

## Application Note 969

### Introduction

A conventional Schottky diode detector such as the Avago Technologies HSMS-8101 requires no bias for high level input power — above 1 mW. However, at low levels, a small amount of DC bias is required for detection to take place. Even though this bias current is at the microampere level, this requirement is often difficult to supply. A Schottky diode has been developed to eliminate this need for DC bias.

### Forward Voltage Characteristic

Since all diodes in this discussion are Schottky diodes, the forward current obeys the equation:

$$I = I_s \left( \frac{q}{e^{nkT}} (V - IR_s) - 1 \right)$$

The ideality factor,  $n$ , is close to unity for these diodes, so the equation may be written:

$$I = I_s \left( \frac{V - IR_s}{e^{.026}} - 1 \right)$$

where the values for the constants  $q$ , electron charge,  $T$ , room temperature, and  $k$ , Boltzmann's constant, have been inserted. The main difference in the behavior of the different types of diodes is embodied in  $I_s$ , the saturation current. There may also be differences in  $R_s$ , the series resistance.

Figure 1 shows the forward current characteristics of the HSMS-8101 Schottky diode and two versions of zero bias diodes, the HSMS-2850 and HSCH-3486. These curves are close to the curves predicted by the diode equation with the constants shown in Table 1. The HSMS-8101, a conventional (n-type or mixer) Schottky requiring DC bias to operate as a detector, stands out from the two (p-type) zero bias diodes with its very low value of saturation current and its low series resistance.

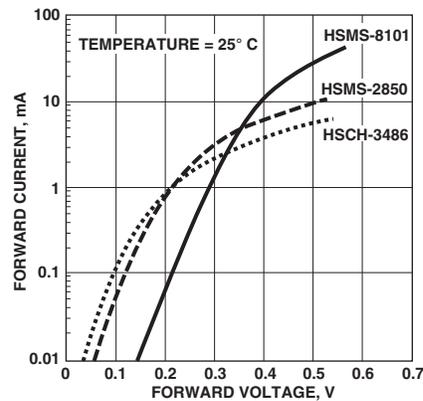


Figure 1. Forward Characteristics of Detector Diodes

Table 1.

Part Number	$I_s$ (Amps)	$n$	$R_s$ ( $\Omega$ )	$C_j$ (pF)
HSMS-8101	$9 \times 10^{-8}$	1.08	4	0.23
HSMS-2850	$3 \times 10^{-6}$	1.06	25	0.17
HSCH-3486	$5 \times 10^{-6}$	1.08	50	0.1

## Voltage Sensitivity

A detector diode may be treated as a current generator across the diode video resistance.<sup>(1)</sup>

The voltage sensitivity,  $\gamma$ , is the product of the current sensitivity,  $\beta$ , and the video resistance, the inverse of the derivative of current with respect to voltage.

$$I = I_S \left( \frac{V}{e \cdot .026} - 1 \right)$$

$$\text{and } \frac{\partial I}{\partial V} = \frac{I + I_S}{.026}$$

### The Perfect Detector

Neglecting parasitic and reflection losses:

$$\gamma = \frac{0.52}{I + I_S}$$

For small values of current:

$$\gamma = \frac{0.52}{I_S}$$

The theoretical current sensitivity is 20 amperes per watt <sup>(2)</sup> so:

$$\gamma = \frac{0.52}{9 \times 10^{-8}} = 5.8 \times 10^6$$

or, for zero bias current:

$$\gamma = \beta / \frac{\partial I}{\partial V}$$

This analysis indicates no advantage in using the zero bias diodes because sensitivity varies inversely as saturation current, and the standard HSMS-8101 diode has the lowest saturation current. In fact, no improvement is needed since the sensitivity is:

$$\gamma_1 = \frac{0.52}{I_S (1 + \omega^2 C_j^2 R_S R_V)}$$

or 750,000 mV/ $\mu$ W.

Since the actual sensitivity of the HSMS-8101 detector with zero bias is close to zero, some major corrections in the analysis are needed. Consideration of the effects of junction capacitance, load resistance, and reflection loss will bring this analysis close to reality.

$$C_j = 0.1 \text{ pF}, R_S = 50 \Omega,$$

$$\text{and } R_V = \frac{0.026}{I_S},$$

$$\gamma = \frac{1,000}{f^2 + 2 \times 10^6 I_S} \frac{\text{mV}}{\mu\text{W}}$$

$$\gamma_2 = \gamma_1 \frac{R_L}{R_V + R_L} = \frac{\gamma_1}{1 + \frac{R_V}{R_L}}$$

### Junction Capacitance

The effect of junction capacitance on current sensitivity has been derived in Section 11.2 of Reference 1. Adding this effect to the voltage sensitivity analysis gives:

$$\gamma_2 = \frac{\gamma_1}{1 + \frac{26 \times 10^{-8}}{I_S}}$$

$$\gamma_3 = \gamma_2 (1 - r^2)$$

For a typical case,

so that:

with frequency in gigahertz and saturation current in amperes. Figure 2 shows how capacitance modifies voltage sensitivity. Since the change is due to the rf current split between  $C_j$  and  $R_V$ , the reduction is more severe at higher frequencies, when the capacitive susceptance is higher. The inverse relationship with saturation current is still present at low frequencies or high saturation current values. However, predicted values of voltage sensitivity are still unreasonably high.

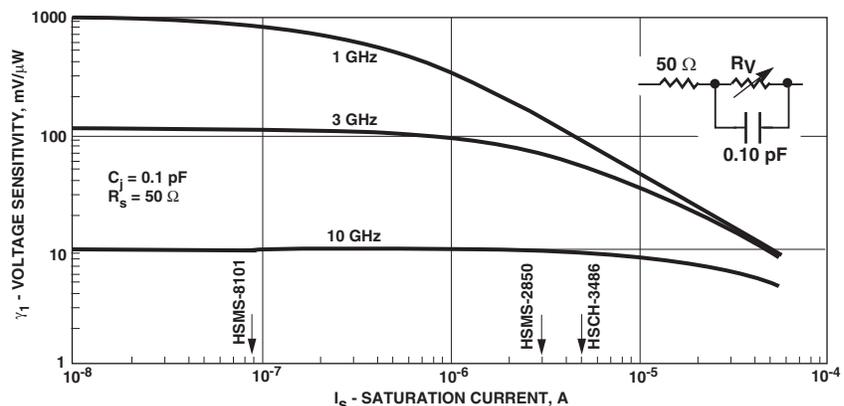


Figure 2. Effect of Capacitance on Voltage Sensitivity

### Load Resistance

A detector diode may be considered as a video voltage source of impedance  $R_V$  feeding a load resistance  $R_L$ . The voltage across the load,  $\gamma_2$ , is reduced by the ratio of  $R_L$  to  $R_V + R_L$

when the ratio of video resistance to load resistance is small,  $\gamma_2 = \gamma_1$ . This is a common condition for biased detectors. However, at zero bias the diode resistance is usually not small compared to load resistance. For a typical load resistance value of 100 k $\Omega$ , the sensitivity is:

The effect of load resistance is shown in Figure 3. The inverse relationship between sensitivity and saturation current in  $\gamma_1$  combined with the direct relationship due to load resistance results in a maximum voltage sensitivity when  $I_S = 3 \times 10^{-7}$  A. However, these theoretical results for  $\gamma$  are still unreasonably high, particularly at the lower frequencies.

$$\rho = \frac{Z_D - 50}{Z_D + 50}$$

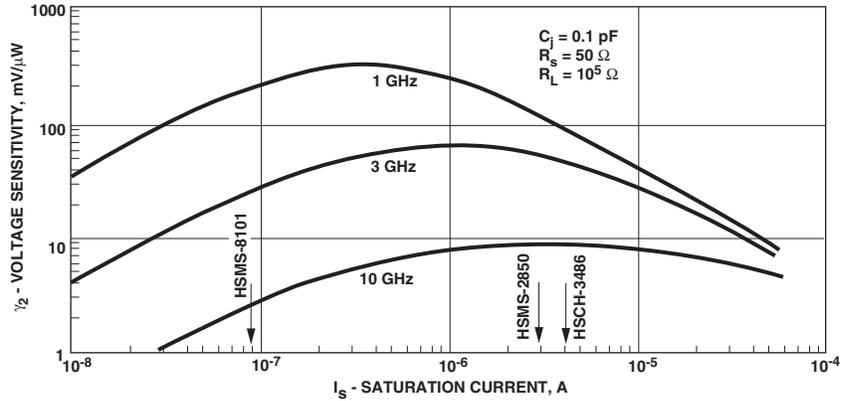


Figure 3. Effect of Load Resistance and Capacitance on Sensitivity

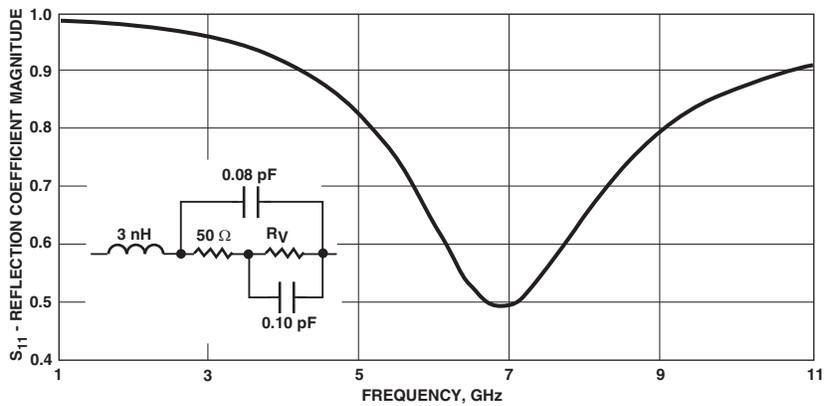


Figure 4. Reflection Coefficient for the HSCH-3486

### Reflection Loss

The analysis so far has assumed that all incident power is absorbed by the diode. Normally this is a good assumption because low loss matching circuits can be designed to eliminate reflection losses. With zero bias detectors, however, the mismatch may be so severe that it is not possible to eliminate these reflection losses. In fact, most of the incident power may be absorbed by losses in the matching network. If we go to the other extreme and assume no matching, the sensitivity becomes:

where  $r$  is the reflection coefficient of the diode.

Assuming the diode impedance,  $Z_D$ , terminates a  $50 \Omega$  system:

The diode impedance is a function of the package parasitics as well as the frequency.

While the calculation of  $\rho$  is straightforward, it requires a knowledge of the diode parasitics. In Figure 4, an equivalent circuit is shown for the HSCH-3486 zero bias Schottky diode, including package inductance and package capacitance. Assuming a value of  $R_V = 5 \text{ k}\Omega$  (calculated from the value of  $I_S$  given in Table 1),  $\rho$  can be calculated as shown in Figure 4. Note that the package inductance resonates with the package and diode junction capacitance to produce a partial impedance match near 7 GHz.

Using these data for  $\rho$ , one can calculate  $\gamma_3$ , as shown in Figure 5, for a diode immersed in a  $50 \Omega$  system without impedance matching networks.

Note that the values of  $I_S$  for the three diodes under discussion are flagged in Figures 2, 3 and 5. However, those three curves were calculated based upon a value of  $R_S$  and  $C_j$  which are typical only of the HSCH-3486.

The effects of the package parasitics show up clearly in Figure 5. Overall values of sensitivity are dramatically

reduced at 1 GHz, where  $\rho$  is very nearly equal to unity. At 3 GHz, sensitivity is reduced, but not by so much as at 1 GHz, with the result that  $\gamma_3$  is higher at 3 GHz than at the lower frequency. The reduction in sensitivity at 10 GHz due to package parasitics is quite small, since  $\rho$  is lower at that frequency for the Avago package number 18.

The calculations and discussions so far make it difficult to directly compare the two zero bias Schottky diodes. Using the equations given earlier, one can calculate  $\gamma_2$  for the HSCH-3486 and the HSMS-2850 as a function of frequency, as shown in Figure 6.

The different values of  $C_j$ ,  $I_S$  and  $R_S$  of the two diodes result in the HSMS-2850 providing greater performance at frequencies below 3 GHz while the HSCH-3486 yields superior sensitivity at higher frequencies.

In theory, one can achieve voltage sensitivities as shown in Figure 6 over a narrow band of frequencies through the use of a low loss impedance matching network at the input to the diode<sup>(3)</sup>. However, this is unfortunately not the case at the lower frequencies where the reactance of the  $C_j$  is low, resulting in a very high value of impedance for the  $R_V - C_j$  parallel combination (in excess of  $1 \text{ k}\Omega$ ). The finest silver-plated stub tuners lack sufficient  $Q$  to match the very high value of  $\rho$  to  $50 \Omega$  — the high standing wave at the diode terminals (output of the tuner) cause losses in the best tuners to rise dramatically. The situation is even worse when the impedance matching network is realized in some lower- $Q$  medium such as microstrip. As a result, a value of  $40 \text{ mV}/\mu\text{W}$  at 1 GHz and  $30 \text{ mV}/\mu\text{W}$  at 3 GHz represent a practical upper limit to  $\gamma$  in real-world detector circuits.

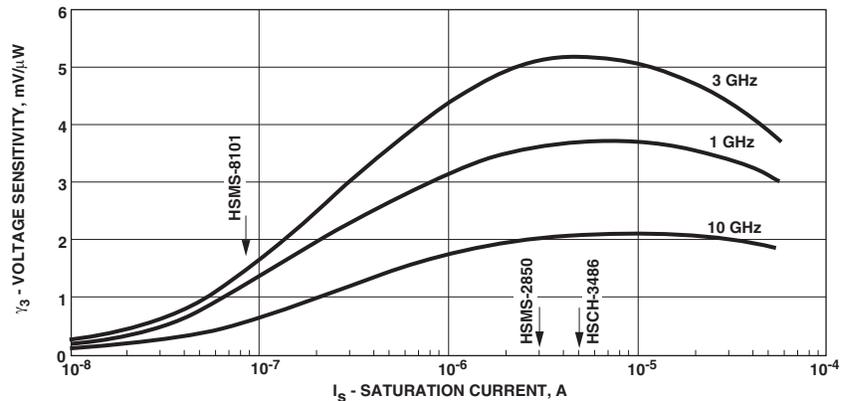


Figure 5. Effect of Mismatch, Load Resistance and Capacitance on Sensitivity

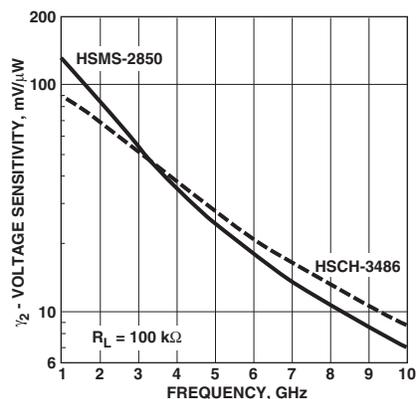


Figure 6. Comparison of Two Zero Bias Schottky Diodes

## Temperature Effects

All of our computations so far have assumed a temperature of 300° K. The first equation in this note indicates that, for a given value of forward voltage, the forward current of a Schottky diode depends upon temperature. The diode's current sensitivity,  $\beta$ , is also a function of temperature<sup>(4)</sup>. Reference 4 gives a good treatment of temperature effects of a Schottky diode with fixed (external) bias. However, in a zero bias Schottky,  $I_s$  is also temperature-dependent, adding an extra variable to the total equation for  $\gamma$  vs. temperature. Thus it is that a complete discussion of the effects of temperature on voltage sensitivity is beyond the scope of this note.

## Measurement of $C_j$ and $R_V$

The Schottky diode (without package) can be represented by the three element equivalent circuit shown in Figure 2.  $R_s$  (50  $\Omega$  in this case) is easily measured. However, the measurement of  $R_V$  and  $C_j$  cannot be done by conventional means.

Typically, the junction capacitance of a Schottky diode is measured at 1 MHz. For a conventional diode such as the HSMS-8101,  $R_V = 0.026/I_s$  is very high, permitting the measurement of junction capacitance at this low frequency. In these diodes, video resistance is determined by the external DC bias which is applied when they are used.

In a zero bias Schottky, however,  $R_V$  is set by the saturation current and is thus an unknown. Moreover, it is the nature of zero bias Schottky diodes that  $R_V$  will be lower than  $1/\omega C$  at 1 MHz, thus shorting out the capacitance and making it impossible to measure by conventional means. Some simple calculations, based upon the diode equivalent circuit shown in Figure 2, will reveal the fact that  $C_j$  for zero bias Schottky diodes must be measured at frequencies much higher than those used in capacitance or impedance bridges.

A convenient method of measurement involves measuring the diode's attenuation as a series element in a 50  $\Omega$  transmission line. Very low power levels are used in an instrument such as the HP8753C (which offers the advantage of a logarithmic frequency scale), resulting in a display like the example shown in Figure 7.

At low frequencies, the video resistance sets the attenuation, since  $1/\omega C$  is so high. However, at VHF (and higher) frequencies, junction capacitance dominates the total attenuation. Straightforward modeling (as shown in the figure) permits the calculation of  $C_j = 0.091$  pF and  $R_V = 7.5$  k $\Omega$  for the sample tested.

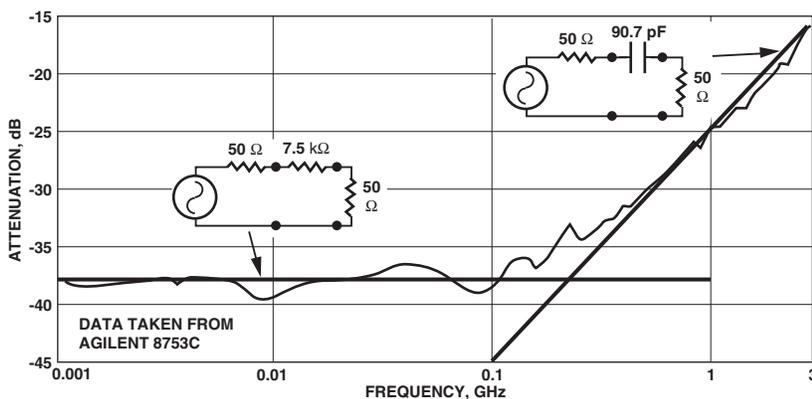


Figure 7. Agilent 8753C Display

## Summary

Detector diodes are most sensitive at zero bias when the saturation current is small, corresponding to large video resistance. However, there is a limit to sensitivity when the resistance is so large that it cannot be matched. An optimum diode is designed to have the proper saturation current. Choice of saturation current involves a compromise between sensitivity due to large resistance and loss due to matching.

## References

1. Torrey, H.C. and Whitmer, C.A., "Crystal Rectifiers", MIT Radiation Laboratory Series, Vol. 15, McGraw-Hill (New York) 1948.
2. Watson, H.A., "Microwave Semiconductor Devices and Their Circuit Applications", P. 379, McGraw-Hill, 1969.
3. Avago Technologies Application Note 963, *Impedance Matching Techniques for Mixers and Detectors*.
4. Avago Technologies Application Note 956-6, *Temperature Dependence of Schottky Detector Voltage Sensitivity*.

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